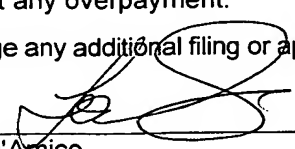


FW

AMENDMENT TRANSMITTAL LETTER			Docket No. R2180.0189/P189	
Application No. 10/769,817-Conf. #9540	Filing Date February 3, 2004	Examiner Ida M. Soward	Art Unit 2822	
Applicant(s): Takaaki Negoro et al.				
Invention: SEMICONDUCTOR DEVICE HAVING DMOS AND CMOS ON SINGLE SUBSTRATE				
TO THE COMMISSIONER FOR PATENTS				
Transmitted herewith is an amendment in the above-identified application.				
The fee has been calculated and is transmitted as shown below.				
CLAIMS AS AMENDED				
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate
Total Claims	23	- 23 =		x
Independent Claims	11	- 11 =		x
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>				
Other fee (please specify):				
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:				0.00
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity				
<input checked="" type="checkbox"/> No additional fee is required for this amendment.				
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed.				
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.				
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.				
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <u>04-1073</u> as described below. A duplicate copy of this sheet is enclosed.				
<input checked="" type="checkbox"/> Credit any overpayment.				
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.				
 _____ Thomas J. D'Amico Attorney Reg. No.: 28,371			Dated: <u>October 12, 2005</u>	
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 2101 L Street NW Washington, DC 20037-1526 (202) 828-2232				



Docket No.: R2180.0189/P189
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Takaaki Negoro et al.

Application No.: 10/769,817

Confirmation No.: 9540

Filed: February 3, 2004

Art Unit: 2822

For: SEMICONDUCTOR DEVICE HAVING DMOS
AND CMOS ON SINGLE SUBSTRATE

Examiner: Ida M. Soward

**REQUEST FOR RECONSIDERATION IN RESPONSE TO NON-FINAL OFFICE
ACTION**

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated July 12, 2005, please amend the above-identified U.S. patent application as follows:

A Complete Listing of the Claims begins on page 2 of this paper.

Remarks/Arguments begin on page 15 of this paper.